
  
**Maharashtra Institute of Technology, Aurangabad**
  
**Department of Electronics and Telecommunication Engineering**

G.S.Mandal's

AU

**Time Table**

Academic Year-2021-22 Part-I (Semester III) with effective from **1st October 2021**  
 Class: Second Year Class Room No.: 425

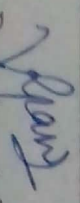
Day/Time	10:30pm to 11:30pm	11:30pm to 12:30pm	12:30pm to 1:15pm	1:15 pm to 2:15 pm	2:15 pm to 3:15 pm	3:15 to 3:30	3:30pm to 4:30pm	4:30pm to 5:30pm
Monday	A&DC (SVV)	V&PDE (SGH)	R	EDC (PPP)	NW&L (KRK)	R	DOS-III / DSD- (A1/A2)-401/405B (SBB/RLG)	
Tuesday	V&PDE (SGH)	EDC (PPP)	E	A&DC (SVV)	DSD (RLG)	E	DSD / A&DC (A1/A2)-405B/412 (RLG/SVV)	
Wednesday	EDC (PPP)	A&DC (SVV)	C	V&PDE (SGH)	TGS	C	A&DC/NW&L (A1/A2)-412/406 (SVV/KRK)	
Thursday	EW-I/EDC (A1/A2)-414/404 (UDS/PPP)		E	NW&L (KRK)	DSD (RLG)	E	NW&L/DOS-III (A1/A2)-406/401 (KRK/SBB)	
Friday	EDC /EW-I (A1/A2)-404/414 (PPP/UDS)		S	DSD (RLG)	NW&L (KRK)	S		
Saturday	Library		S			S		

**Class Teacher: P.P.Ms.P.P.Patil**

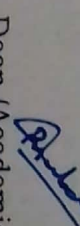
Name and location of lab	
Research Lab	401
Electronic circuit Lab	404
Control System Lab	405A
Power Electronics Lab	406
Computer System Lab	413


**Course Teachers:**

- 1) SVV:Mrs.S.V.Verma
- 2) RLG:Mr.R.L.Gaike
- 3) KRK:Mrs.K.R.Khandagale
- 5) UDS:U.D.Shirale
- 6)SBB:Mr.S.B.Biradar
- 7) SGH:Mr.G.G.Hiwale

  
 Departmental  
 Time table coordinator

  
 H.T.C.E.D.

  
 Dean (Academics & QA)

  
 Director