Curriculum Vitae

Name:Mrs Lata Gangadhar Sase

Address: ''Parth'' Plot no 23, Gut no 139, Chhatrapati Nagar,Beed

Bypass, Aurangabad.

E-mail: lata.padole@mit.asia
Mobile: 9422691069
Date of Birth:08/06/1983



Academic Credentials

Class/	Specialization	Institution	University	Year	%/CGPA	Class
Degree						
Ph.D.						
M.E./ M. Tech.	Communication	MIT,Auran	B'AMU	2016	63.63	First class
	Engineering	gabad				Tirst class
B.E./B.Tech.	Electronics and	STB	B'AMU	2005	63.33	
	Telecommunica	college of				First
	tion	Engineerin				Class
	Engineering	g,Tuljapur.				

M. Tech. Project: Design And Implementation Of Intelligent Solar Hybrid Inverter Utilizing Pv Energy.

Experience

Sr.	Organization	Post	From	To	No. of
No.					Years
1	Marathwada Institute of technology, Aurangabad	Lecturer	17/01/2007 to 28/07/2007		7 months
2	Maharastra institute of technology Aurangabad	Assistant Professor	28/07/2007		13 years

List of Courses Taught/Teaching at UG level:

- Basic Electronics
- Digital Electronics
- Digital Signal Processing
- Linear Integrated Circuits and Applications
- Analog Communication Engineering
- Digital Communication Engineering
- Electrical Engineering and Elements
- Electrical Machines
- Power Devices and Machines
- Electronic Measurements
- Computer network and Security
- Signals and Systems

List of Courses Taught/Teaching at PG level

• Antenna Theory and wave propogation

Additional Assignments/Duties

- Seminar co-ordinator
- LRC co-ordinator
- Class test co-ordinator
- Anti-ragging co-ordinator
- Worked as US for university examination
- Physical verification committee
- Class test verification committee member

Membership of Professional Bodies

IETE life member

Seminar/Workshop/ Industrial Training/ STTP//FDP/CEP/Conference Attended

Sr	PROGRAM	PLACE	DURATION
No.			
1	PLC PROGRAMING	GOV. POLYTCHNIC	4/01/07 TO 18/01/07
		A'BAD	
2	FACULTY DEVELOPMENT	M.I.T. A'BAD	4/02/08 TO 6/02/08
3	DESIGN AND IMPLEMENTATION OF	M.I.T. A'BAD	30/06/08 TO 4/07/08
	ADVANCED EMBEDDED SYSTEM		
4	CHALLENGES IN CMOS VLSI DESIGN	M.I.T. A'BAD	08/11/08 TO 10/08/08
5	INTRODUCTION TO MATLAB	M.I.T. A'BAD	14/07/09 TO 17/07/09
6	EMERGING TRENDS IN MICROWAVE	M.I.T. A'BAD	07/05/12 TO 11/05/12
	AND MICRO STRIP DEVICES		
7	Foundation Program in ICT for Education by	M.I.T.Aurangabad	8/03/2018 to 12/04/2018
	IIT		

List of Research Publications

Papers in National/International Journal

- "Design of Solar Inverter Using MPPT Technique" in International Journal of Engineering Science Research Technology ISSN No: 2277-9655 January, 15, 2016
- "Solar Inverter In Grid Oriented System Using Maximum Power Point Tracking" in International Journal of Engineering Science Research Technology ISSN No: 2277-9655, Impact Factor: 4.116, November, 17, 2016.

Date: 24/02/2020

Place: Aurangabad