

Curriculum Vitae

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Date of Birth: 09th November 1986

Academic Credentials

Class/ Degree	Specialization	Institution	University	Year	%/ CGPA	Class
Ph.D.	Electronics Engineering	-	Dr. Babasaheb Ambedkar Marathwada University, Aurangabad	-	-	Pursuing
M. Tech.	Electronic Design and Technology	DOEACC Centre (Formerly CEDTI), Aurangabad	Dr. Babasaheb Ambedkar Marathwada University, Aurangabad	2011	69.63%	First with Distinction
B.E.	Electronics and Telecommunication	Hi-Tech Institute of Technology, Auranagabad	Dr. Babasaheb Ambedkar Marathwada University, Aurangabad	2008	67.25%	First with Distinction

M. Tech. Project:

□ Project Title : "PCR Thermal Cycler"

Description: In PCR Thermal Cycler, biological samples can be thermally cycled through temperature-time protocol that includes denaturing DNA into single strands, annealing primers to the single strands, and extending those primers to make new copies of double-stranded DNA. Typical thermal cycler utilizes active cooling through the use of thermoelectric coolers such as Peltier devices. In our project we utilize air forced convection for cooling and Induction heating for heating.

U.G. Project:

□ Project Title : "VLSI BASED REMOTE PROCESS CONTROL"

Description: In our project, the process will have a closed loop intelligent system that will continuously monitor different parameters such as level and temperature. The current parameter readings are shown on a display module. The moment any parameter crosses the set point and a relay is energized to take control of the process. An SMS, with the details of the process parameters is sent to the Field Engineer. In response, the Engineer can send SMS back to the system. The SMS will be analyzed by the PC, authentication procedure carried out, and an appropriate control action be taken. i.e. the set point is changed. This project is applicable for any chemical or cement industry for monitoring and controlling the parameters such as temperature, pressure and water level etc. through the SMS system.

Ph. D. Research: Image Processing and Neural Network

Key Research Areas: Internet of Things, Image Processing, Embedded System Design

Experience

Sr. No.	Organization	Post	From To	No. of Years
01	Maharashtra Institute of Technology, Aurangabad	Assistant Professor	28 th May 2012 to till date.	7.9
02	K. K. Wagh College of Engg. Education &	Assistant Professor	28 th December 2011 to 28 th May 2012.	0.5

	Research, Nashik			
03	Savitribai Phule Women Engg. College, Aurangabad	Assistant Professor	1 st August 2011 to 27 th December 2011	0.5
04	FDC Limited, Aurangabad	Maintenance Officer	09 th November 2008 to May 2009	0.5

List of Courses Taught/Teaching at UG level

- Internet of Things
- Electronic Circuit Design Technology
- Power Devices & Machines
- Basic Electronics
- Basic Electrical Engineering
- Power Electronics
- Telecommunication and Switching System
- Wave Theory and Antenna
- Computer Simulation and Tools
- Advanced Digital Signal Processing
- Microprocessor

Additional Assignments/Duties

- Working as a Class Test Coordinator
- Working as a GIZ-MASSIA project coordinator
- Working as a DSP/VLSI Lab In-charge
- Working as a Industry Institute Cell coordinator
- Working as a NBA Core Committee member
- Working as a Virtual Lab Coordinator
- Working as a Robotics Club coordinator
- Worked as a departmental website coordinator
- Worked as a departmental E-magazine coordinator
- Worked as a NAAC Core Committee Member
- Worked as **FY Admission committee member** during academic year 2016-2017

- Worked as **Laboratory Equipment verification Coordinator** during academic year 2016-2017
- Worked as **Laboratory Equipment Repair Coordinator** during academic year 2016-2017
- Laboratory equipment checked and repaired as per the report given by Laboratory equipment verification committee.
- Laboratory equipment such as Power Supply, Basic Electronics Lab Kits, Microcontroller Lab Kits, Power Electronic Lab Kits etc. **around 38 Kits are repaired.**
- Worked as **Coordinator for Industry oriented Training Program** during academic year 2016-2017
- Worked as Coordinator to organize proposed International Conference on Digital Signal and Image Processing (ICDSIP) -2017
- Worked as Lab In-charge : DSP/VLSI (Lab 403)
- Worked as **FY M. Tech Admission committee member** during academic year 2015-2016
- Worked as **Under Study for the University exam conducted by Dr. Babasaheb Ambedkar Marathwada University, Aurangabad - May/June-2015.**
- Worked as **Project Coordinator** during academic year 2015-2016
- Worked as **Coordinator for Industry oriented Training Program** during academic year 2015-2016
- Coordinated **Two days National Level Workshop** on “**Research Methodology**” organized by Maharashtra Institute of Technology during academic year 2015-2016.
- Coordinated **Four days National Level Workshop** on “**MATLAB, Simulink and Low Cost Design using Signal and Image Processing Toolbox**” on 16th to 19th December 2015 organized by Maharashtra Institute of Technology.
- Worked as PA System Coordinator during academic year 2015-2016
- Worked as **Class Test Coordinator** during academic year 2014-2015.
- Worked as **FY Admission committee member** during academic year 2014-2015.
- Worked as **FY M. Tech Admission committee member** during academic year 2014-2015
- Worked as **Class Teacher of FY(E)** during academic year 2014-2015
- Worked as **Under Study for the University exam conducted by Dr. Babasaheb Ambedkar Marathwada University, Aurangabad - Nov/Dec-2014.**

- Worked as **Class Test Coordinator** during academic year 2013-2014
- Worked as **Class Test Coordinator** during academic year 2012-2013

Membership of Professional Bodies

- Life Member of “**Indian Society for Technical Education (ISTE)**” Member No. **107490**

Computer/Software Proficiency

- Programming Languages: **C, Python, VB 6.0, and HTML**
- **MIT app Inventor**
- **CADSTAR,**
- **Proteus, Circuit Maker, Eagle, ExpressPCB**
- **MATLAB**
- **Keil, MPLAB, Arduino**
- **MS-Office**

Seminar/Workshop/ Industrial Training/ STTP//FDP/CEP/Conference Attended

- Two Weeks Certificate course in “**PLC Programming**” at **Indo German Tool Room (IGTR), Aurangabad.** during academic year 2013-2014
- Successfully completed Two weeks **In-plant Training** at **Indo German Tool Room (IGTR), Aurangabad** in 2013.
- Attended “**Two weeks workshop on Electronic Manufacturing and Product Design**” in June 2012 organized by **CDAC and NIELIT Aurangabad.**
- Attended “**Two days workshop on Network Theory and Design**” in July 2013 organized by **ICEEM, Aurangabad.**
- Attended workshop on "**INDUSTRIAL NETWORKS & COMMUNICATION**" organized by **Pimpri Chinchwad College of Engg., Pune.**
- Participated as Industrial Project Exhibitor in the “**INTERNATIONAL ELECTRONICS EXPO-2011**” held at Delhi organized by Electronics For You.

- Attended workshop on "**INTELLECTUAL PROPERTY & PATENT LIFE CYCLE MANAGEMENT**" organized by **K. K. Wagh College of Engg. Education & Research, Nashik.**
- Attended Workshop on "**EMBEDDED SYSTEM & IT'S APPLICATION**" organized by **Hi-Tech Institute of Technology, Aurangabad.**
- Participated in "**National Level Technical Project Competition Creation-2008**" organized by **Hi-Tech Institute of Technology, Aurangabad.**

Seminar/Workshop/ Industrial Training/ STTP//FDP/CEP/Conference organized

1. Delivered guest lecture on IoT and its applications at Puranmal Lahoti Government Polytechnic, Latur, February, 2019
2. Worked as **Coordinator for Industry oriented Training Program** during academic year 2016-2017
 - Conducted Training Courses:
 - **Integrated course in PLC, SCADA & Pneumatics**, by Indo German Tool Room (IGTR), Aurangabad (A Govt. of India Society under Ministry of MSME) at MIT(T) Campus.
 - Duration : 45 days
 - **Revenue generated from this activity : Rs. 17,700/-**
 - This training program enhances skill of the student and also **benefit in In-Plant Training**
3. Coordinated **Four days National Level Workshop** on "**MATLAB, Simulink and Low Cost Design using Signal and Image Processing Toolbox**" on 16th to 19th December 2015 organized by Maharashtra Institute of Technology
4. Coordinated **Two days National Level Workshop** on "**Research Methodology**" organized by Maharashtra Institute of Technology.

Invited talks delivered

1. Worked as **Coordinator and Speaker** during academic year 2016-2017
 - **Maharashtra Institute of Technology Robotics Academy (MITRA) Six Days Workshop on Robotics** during 2/05/2016 to 10/05/2016
 - This workshop aims to aware school going student about the Robotics and field Electronics Engineering.

2. Worked as **Coordinator and Speaker** during academic year 2016-2017

- Coordinated & conducted **Six days Workshop** on “**Electronics Circuit Simulation, PCB Design and fabrication**” organized by Electronics & Telecommunication Engineering Department : 23/09/2016 to 09/10/2016
- This workshop conducted to **improve PCB designing and fabrication skill of the student** and it helps student while doing final year project.

3. Worked as **Coordinator and Speaker**

- Coordinated & conducted **Three days Workshop** on “**PCB Design and fabrication**” organized by Electronics & Telecommunication Engineering Department. From 1st Feb.2015 to 7th Feb.2015.
- This workshop conducted to **improve PCB designing and fabrication skill of the student** and it helps student while doing final year project.

Projects, Research Grants and Consultancy

- Ensure Regrinding of Tools at Specified frequency
- Inspection of Defective Object using Computer Vision Image Processing
- Process improvement in SMD LED placement and soldering

Awards, Achievements and Recognition

1. **Passed Ph.D. Entrance Test, PET-2012** conducted by **Dr. Babasaheb Ambedkar University, Aurangabad.**
2. **Passed Ph.D. Entrance Test, PET-2012** conducted by **Rashtrasant Tukadoji Maharaj Nagpur University, Nagpur.**
3. **Passed Ph.D. Entrance Test, PET-2014** conducted by **Dr. Babasaheb Ambedkar University, Aurangabad.**
4. Published paper on "**DIGITAL TV DEMYSTEIFIED**" in **Electronics For You Magazine, Oct-2011.**

Date: 25/02/2020

Place: Aurangabad